

A METHOD FOR METALIZING WAFERS

FIELD OF THE INVENTION

This is a continuation-in-part application of currently co-pending Application No. 10/307,510, filed December 2, 2002. 5

BACKGROUND OF THE INVENTION

US Patents # 4,199,623 to Nuzzi et al, # 3,993,491 to Feldstein and # 4,082,557 to Pizzo are referenced herewith, in addition to prior art patents referenced in the co-pending application No. 10/307,510, published as US-2003- 10 0233960, the contents of which are incorporated herein by reference. Briefly summarizing above referenced patents, they teach the use of silver as a catalyst for electroless deposition (Pizzio), or compositions comprising both copper and stannous ions that are then reduced on the surface of the polymer to act as electroless copper catalysts (Feldstein), or a given ratio of complexes of cuprous 15 and cupric ions (Nuzzi) , said cupric/cuprous ions are then reduced on the polymer surface to form an electroless catalyst. Above referenced patents have not achieved reduction to industrial practice, and in a way teach away from this instant patent.

It is noted that the prior art of electroless plating over polymer surfaces , 20 predominantly relies on immersion techniques. In immersion plating, the workpiece to be plated is dipped sequentially in a series of processing solutions separated by water rinses, to minimize contamination by "drag-in", a term used to denote undesired carrying by the workpiece of a given processing bath or solution to the next. 25

Some of the shortcomings of prior art immersion-based electroless plating techniques are:

1. Excessive and costly consumption of rinse water, a burden on the environment. this patent minimizes rinsing needs/problems.

2. Use of costly and cumbersome automated equipment designed to immersing the workpiece in the many processing solutions and rinsing stations that separate them. This invention can use equipment already available in wafer fab lines.

3. Immersion techniques necessitate relatively large volumes of processing solutions, a special disadvantage with semi - or unstable electroless plating compositions, i.e. formaldehyde-based electroless copper. 5

SUMMARY OF THE INVENTION

Above pending application No. US-2003-0233960 discloses methods and compositions that electrolessly deposit metals, i.e. copper, on non- 10 metals, especially polymer-based Printed Circuit Boards (PCBs). The methods of above application comprise immersing the non-metallic substrate in an aqueous solution of reducible metal ions, followed by immersion in a reducing solution, preferably DMAB solutions comprising ppm levels of copper or silver ions that are shown to "energize" the reducing action of DMAB. Above pending 15 application postulates that said reducing solution is "energized" or enhanced by way of hydrogen that is generated in said DMAB solution, formation of metal hydrides on the surface to be metalized, or a combination of both. The reduced metal, or the metal hydride thus formed on the non-metallic surface, then serves as activator or seeder for electroless deposition of copper. 20

The present application focuses uniquely on metallization or electroless plating of Si-based wafer substrates, more specifically semiconducting Si/SiO₂ wafer substrates, used in the fabrication of ULSI devices. Indeed, wafer substrates afford numerous, novel processing possibilities. Wafers are unique, among other reasons, by virtue of their temperature resistance, which enables the use of 25 elevated processing surface temperatures that are inapplicable with polymer substrates. Indeed, polymers are known to be prone to heat degradation. The surface of the wafer can thus be heated to a predetermined, optimal temperature, well in excess of 100 deg.C (the approximate b.p. of aqueous compositions), prior to being contacted with a solution, or during its contact with the solution. 30

Also, the wafer surface can be cleaned via plasma or other gaseous techniques, often a preferred option for cleaning wafers prior to processing, especially prior to metalization.

It is a central consideration of the present invention to provide methods and compositions that are uniquely and specifically suited for wafer metalization from aqueous media, as distinct from aqueous metallization of polymers via immersion. 5

It is an object of this patent to provide an improved method for metallizing or electrolessly plating semiconductor substrates without precious metal seeding, or activation, by depositing thereon a non-precious metal or a non-precious metal derivative such as metal hydride, which then acts as seed or catalyst in subsequent 10 electroless deposition.

It is another object of this invention to provide a method to aqueously metalize silicon wafers, the method comprising rotation or spinning of the wafer during at least one step of the metalization process, or preferably at times, throughout the entire metalization cycle. Above improved metalization method 15 is not unlike techniques used in ULSI imaging of photoresists.

A further object of this patent is to minimize or eliminate immersion steps /handling of wafers to be metalized, by preferably dispensing onto the wafer substrate at any stage of the metalization sequence, a solution, or water, as required by the metalization process, thereby replacing immersion step or steps. 20

A still further object of this invention is to repair metal defects in ULSI devices, especially as they relate to copper dishing.

A yet additional object of this invention is to allow "direct writing" of metal patterns, i.e. conductors, on a wafer substrate without first patterning an image via photoresist. 25

Another preferred object of this invention envisions electroplating copper (without first electrolessly plating a metal, for example copper) on the wafer substrate, following deposition of a layer or fraction of layer comprising non-precious metal or non precious metal derivative i.e. metal hydride.

Thus, the present invention provides a method of electrolessly depositing a metal on at least a part of the surface of a silicon wafer substrate, comprising:

(a) contacting the silicon wafer substrate with a solution comprising non-precious metal ions so as to obtain a wafer substrate covered with non-precious metal ions; and

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(b) exposing the wafer substrate obtained in step (a) to a reducing solution comprising a reducing agent for reducing the metal ions that cover said substrate to a lower oxidation state.

The method of the present invention visualizes dispensing small/minimal amounts of liquid solution onto the wafer substrate. After the desired 10 interaction/reaction at the wafer/puddle interface is completed, the liquid contained in the puddle is spun off by rotating the wafer at a given speed, leaving the wafer surface essentially dry, and devoid of residual liquid on the surface. The wafer substrate is then ready to receive the solution dictated by the next process step.

In addition to minimizing water consumption, above embodiment of the 15 invention potentially reinforces the metalizing reaction by leaving the wafer substrate with a greater concentration of reacting compound, than would be the case if immersion rinsing took place. An indication of such preferred reaction can be found in the co-pending application, by comparing example 1 vs. example 2.

It is noted that electroless copper plating can be embodied via compositions 20 enumerated in co-pending application no. 10/307,510 , or using a host of other compositions of the prior art, the majority of which rely on formaldehyde as the main reducing agent. This instant application proposes using preferably, but not limitingly, hypophosphite-based electroless compositions as the principal reducing agent for depositing either electroless copper, electroless nickel, or alloys thereof. 25 The use of hypophosphite as possibly the reducer of choice (as opposed to formaldehyde), is of special importance in embodiments of this instant application calling for heating the wafer substrates, or supplying radiant energy to the wafer substrate, before, during, or after its contact with the electroless solution. Indeed,

formaldehyde compositions are environmentally objectionable, having been suspected in the literature as potentially carcinogenic.

Among the benefits of the present invention is that it provides a process where the immersion in at least part of the process steps, is replaced by contacting the wafer with a "puddle" of the desired solution or rinse. By puddle, it is meant 5 that a given solution is dispensed on the wafer surface forming a pool (or puddle), not unlike techniques practiced in photoresist imaging. The wafer surface itself, thus acts as the "container " or "vessel", where a given surface reaction can take place.

The liquid dispensed on top of the wafer substrate by "framing" or 10 surrounding the wafer periphery with a circular dam or "wall", allows the use of increased volumes of solution, volumes that are not afforded by the pool or puddle as described previously. It is to be understood that the term puddle in this invention, includes the embodiment of above proposed "raised wafer periphery" as well, affording larger pool volume when needed. 15

DETAILED DESCRIPTION OF THE INVENTION

This instant invention exploits the very unique features that are inherent in silicon-based wafer surfaces, mainly "spinnability" and heat resistance.

Again as mentioned before, a preferred embodiment of the invention 20 contemplates dispensing onto the surface of the wafer (as it is preferably, though not limiting, mounted on a spinner head) a desired amount of a given solution required by the metalizing process, allowing it to form a meniscus, pool, or puddle, which is a substitute for immersing the wafer in said solution.

The term Puddle, Puddle Processing (PP) as used in this invention, denotes 25 placing a desired amount of liquid solution on the surface of the wafer substrate, where it forms a pool or puddle, allowing the puddle to stay in contact with the wafer substrate for period of time sufficient to achieve a given result or reaction. The puddle can be delivered onto the wafer substrate by means of automated spay

nozzles, or somewhat crudely, by manually using a pipette filled with said liquid solution, or by any other means.

Also, the wafer can be stationary during or after delivery of the puddle, it can slowly/moderately rotate or spin during and/or after delivery to promote spreading of the liquid solution to the periphery of the wafer, or both. The operator 5 can alternately use rotational or stationary wafer processing, repeating it intermittently, if needed to deliver fresh processing liquid.

The height of puddle or pool can be adjusted/controlled via the surface tension of the liquid solution to be dispensed on the wafer substrate, by the previously described construction of a "dam" around the periphery of the wafer, or 10 both.

US Patent #6,372,408 to Lawson is a potentially helpful example of similar techniques applied to photoresist development.

Again, the term "Puddle Processing " (PP) as used in this invention, denotes dispensing a predetermined quantity of a given liquid onto the center of a stationary, 15 or a minimally rotating wafer substrate that promotes spreading of the liquid across the wafer surface, allowing it to be in contact with the wafer surface for a desired period of reaction time to complete the wafer/solution interaction, then spinning or rotating the wafer at a given speed, causing centrifugal forces to remove the liquid off the wafer periphery. 20

The liquid dispensed onto the center of a wafer can be water (for example for rinsing), an organic solvent to facilitate drying, or a chemical composition desired to achieve a given surface reaction.

Also, in order to speed up and facilitate a given surface reaction with the liquid, the wafer surface can be heated to a given desired surface temperature prior 25 to dispensing a given liquid onto it, or following the dispensing of above liquid, or both. Indeed, the flatness of the wafer surface conveniently enables supplying radiation energy, such as for example, laser energy to the solution/wafer interface. In choosing the optimal type of energy, one can form draw drawing on the wealth

of prior methods used in radiation chemistry for polymers, photopolymers, solid state, etc.

Again, after allowing the liquid to stay in contact with the wafer surface for a desired time period, and at a given temperature needed to achieve the desired result, increasing the rotational speed of the surface spins off the liquid. 5

Further, while a preferred embodiment of the patent envisions the use of puddle or PP in all steps leading to metalization, it also contemplates the use of a combination of immersion and PP where it offers process advantages.

Also, in cases where a given amount of liquid dispensed on the wafer surface is insufficient to achieve a desired result, the invention can be embodied by 10 spinning off the "exhausted", and applying a fresh quantity thereof, with or without rinsing in-between, referring to such method as multiple puddle. Multiple puddle may be especially advantageous in the case of electroless solutions applied to the wafer surface, as it insures supply of fresh/ uncontaminated and unexhausted solution. 15

The invention contemplates applying energy to the surface of the wafer during or after a given process step to enhance a given reaction, or for the purpose of annealing a metal deposit to reduce stresses during or after electroless or electroplating of copper, etc.

A preferred embodiment made possible by this invention is the option to 20 repair defects of interconnects metals such as copper. It envisions dispensing an as-needed, possibly small amount of metalizing solution, i.e. electroless copper, to a pinpointed defective area, in order to deposit the needed additional thickness of metal. Deposition or metalization can be synergistically enhanced by delivering radiant energy to the "pinpointed" defective spot to be "repaired" by electroless 25 metalization. Radiation energy can be delivered before, during, or after metalization of said area. This embodiment can serve as a very convenient tool for repairing dishing defects before, during or following CMP operations.

As mentioned previously, the patent enables the generation of metal patterns directly on the wafer surface, without first resorting to photoresist imaging. This 30

can be accomplished by digitally and selectively scanning the seeded or activated wafer surface with an appropriate laser, wherein the laser forms an image or pattern, as it deactivates or activates the seeded wafer surface. The thus formed image, is then reinforced by electroless plating. For example, it is postulated that metal hydride covering the wafer surface will be decomposed or "decatalyzed" when 5 interacting with a computer commanded laser. Such areas will not trigger or catalyze electroless deposition. It is thus contemplated that areas that have been scanned with laser will not plate in electroless bath because the activator on the wafer substrate (i.e. metal hydride) has been 'deactivated', whereas areas not exposed to the laser will indeed plate, thereby selectively forming a metalized 10 pattern. In selectively patterning an electroless image on a wafer substrate, one can be aided by the prior art of digital printing, and /or direct write photoresist imaging as practiced in the area of printed circuits (PCBs).

In practicing this invention, one can be guided by teachings and Examples disclosed in co-pending Application No. 10/307,510, and 15 incorporated herewith by reference. Persons skilled in the art will of course find other means accomplish desired objectives of the invention in fabricating ULSI devices.

At the risk of being redundant, it is stated again, that the very detailed disclosure of the chemistry and mechanisms described in the sections of 20 "Summary of the Invention" and "Detailed Description of the Invention" of pending application no 10/307,510, will be conveniently relied-upon in achieving many, if not all the objectives of this instant invention . It is of course anticipated that one skilled in the art will make the necessary adjustments and refinements, as dictated by the various objects to be achieved in metalizing wafers 25 for use in ULSI devices.

The following steps will generally be followed in pursuing the various objects of the invention:

1. The wafer substrate will be "equipped" with, supplied with, metal nuclei, or nuclei of metal derivatives, such as metal hydrides. Metal hydrides, for example, can be obtained on the wafer substrate by contact with a reducing solution, i.e. oranes, comprising copper ions, silver ions, or ions of lanthanides, as proposed in referenced pending application No. 10/307,510. Alternatively, one can use compositions of metal hydrides described and used in conjunction with hydrogen fuel cells. 5

2. The wafer substrate is next contacted with formaldehyde type, or hypophosphite type electroless composition to achieve desired thickness of metal. 10

3. Using computer control, the wafer substrate is selectively irradiated, in order to define the desired pattern or image, after step #1 above, or after step #2., above. The latter option is at times preferred, and can be embodied via powerful laser, that will remove /burn thin (several angstrom thick)metal layer, i.e. 15 copper, off the wafer surface. Once an image is delineated, additional copper thickness is obtainable by further contacted the thus patterned wafer with electroless solution.

When the substrate is selectively irradiated following step 1 above, the patterned wafer surface is contacted with electroless solution until the desired 20 thickness is achieved.

4. In pursuing "pinpointed metalization", for the purpose of repairing copper defects, the wafer is supplied selectively with electroless composition, at the pinpointed spot, or the electroless solution can be delivered to the entire wafer 25 substrate, followed by pinpointed irradiation, i.e. laser, of the spot to be repaired, said irradiation serving to trigger copper deposition precisely and exclusively at the desired spot that is to be repaired.